./

GENESIS - Learning Report

Embedded c



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| **Ver. Rel. No.** | **Release Date** | **Prepared. By** | **Reviewed By** | **To be Approved** | **Remarks/Revision Details** |
| 1 | 02/01/2021 | R. Usharani |  |  |  |
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**Details**

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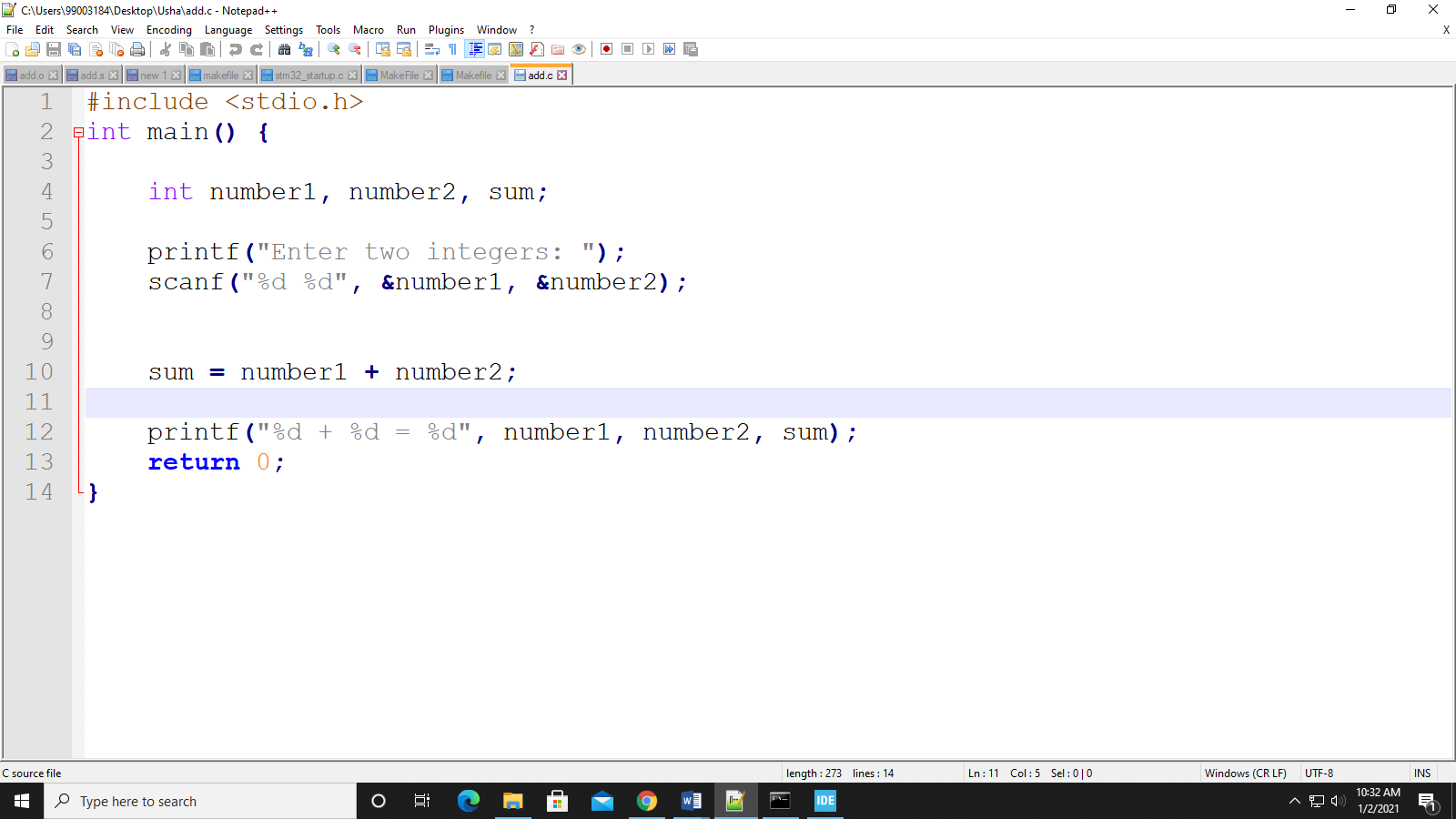
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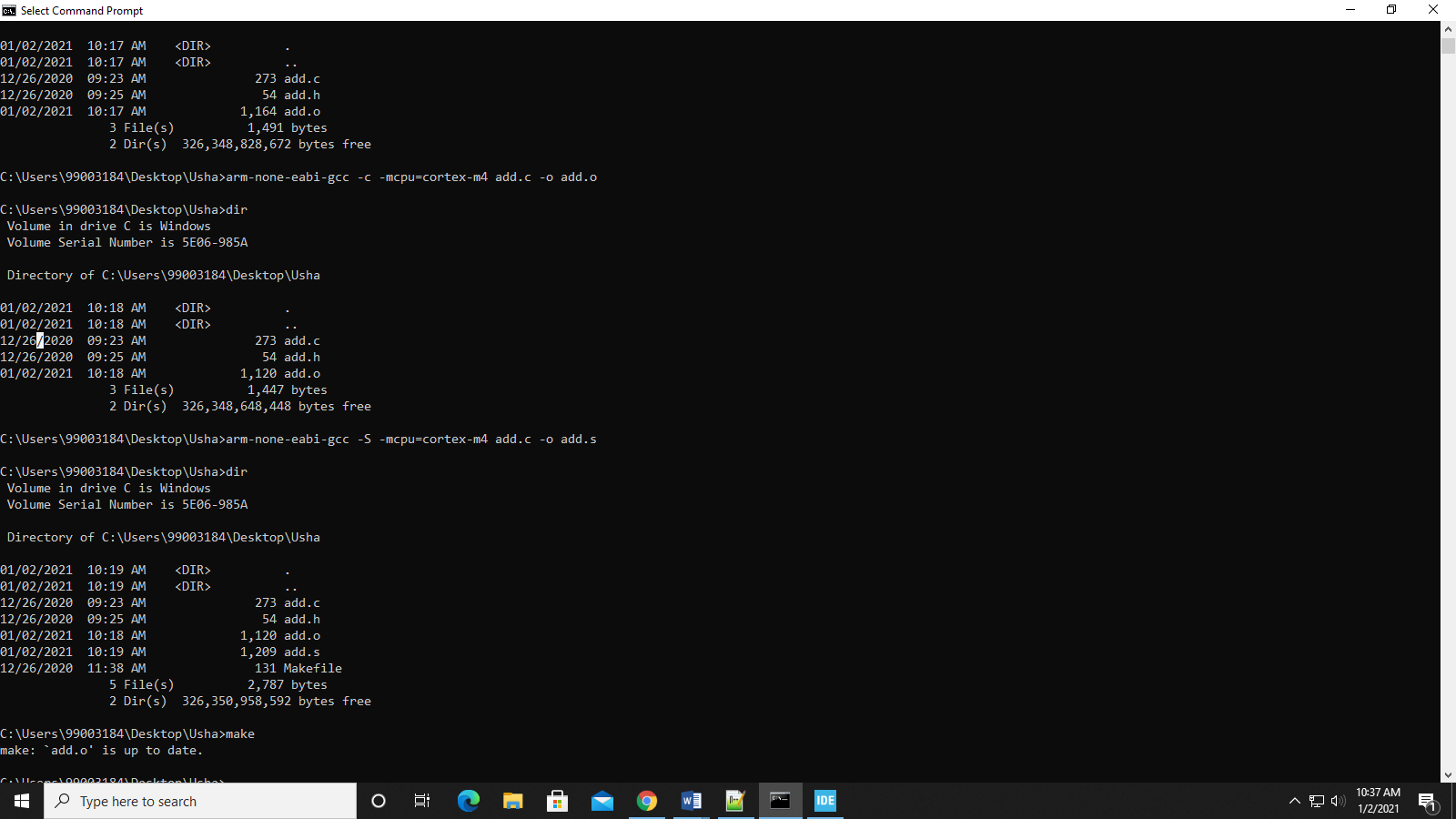
Activity 4: Make file 8

**Activity-1:**

Build System which is able to create the final executable and compiling it in make file using command prompt of window.

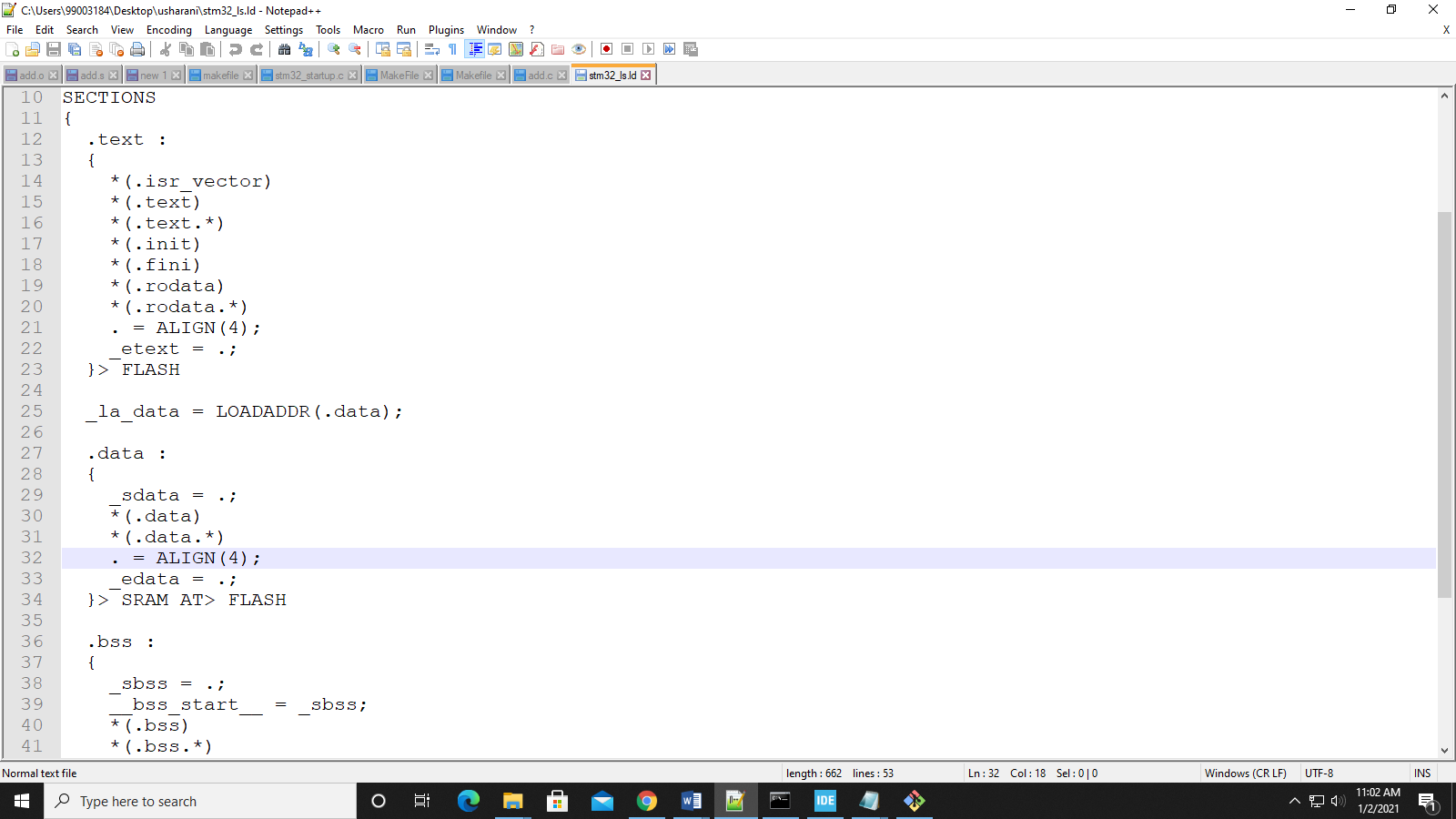


**Makefile for build process:**

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**Activity-2:**

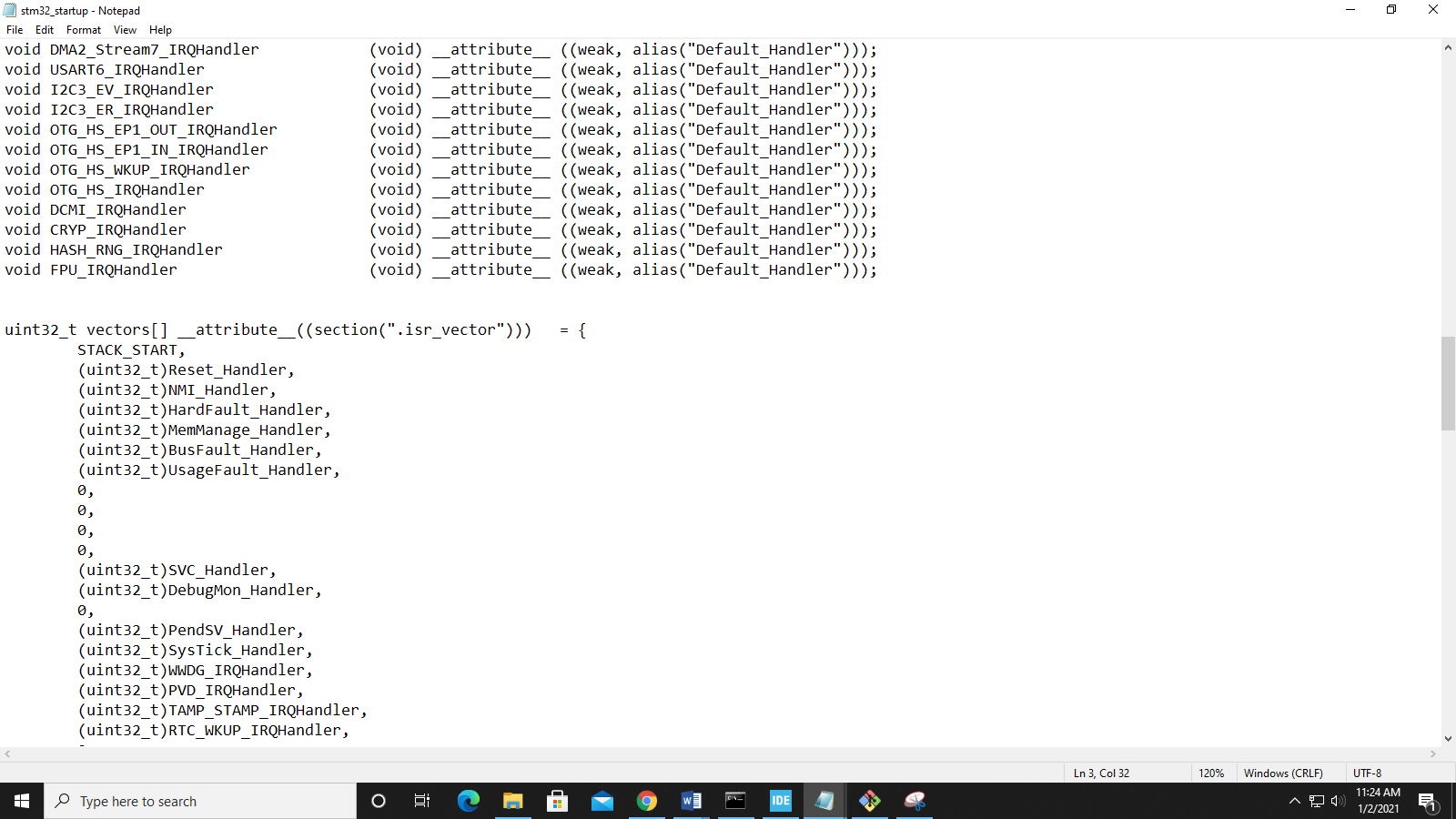
**Linker Script:**

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**Activity-3**:

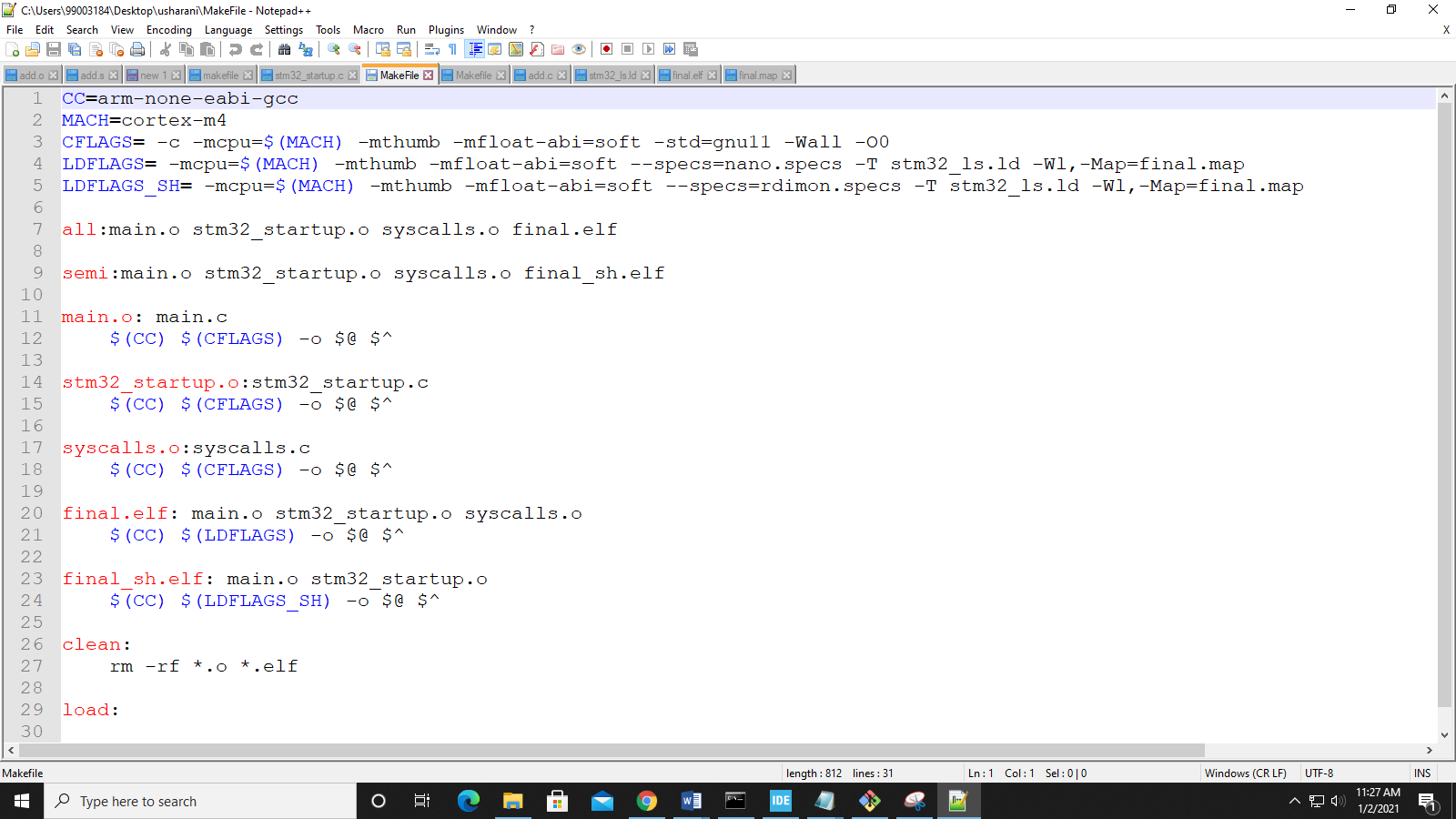
**Start-up Code:**

Various Address of Interrupt Subroutine Vectors(ISR) and its priorities of ISR’s:



**Activity-4:**

**Makefile:**

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**Github link:**

<https://github.com/99003184/Embedded-C>